



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HIRAOKA, Tetsuya, et al.**

Group Art Unit: **2827**

Serial No.: **09/973,002**

Examiner: **Lourdes C. Cruz**

Filed: **October 10, 2001**

P.T.O. Confirmation No.: **4785**

For. **SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS**

REQUEST FOR APPROVAL OF DRAWING CHANGES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

Date: **May 30, 2003**

The Examiner's approval of the drawing corrections indicated in red ink on the attached sheet of drawings is respectfully requested.

Upon receipt of approval of the drawing corrections and a formal Notice of Allowance, a bonded draftsman will be retained and the appropriate corrections will be made.

In the event any fees are required in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Attachment: 2 Sheets of Corrected Drawings (Figs. 1 and 2)